



2G DDR2 800 LO 1.8V

2GB 2Rx8 256M x 64-Bit DDR2-800 CL6 240-Pin DIMM

SPECIFICATIONS

Clock Cycle Time (tCK)	2.5ns (min.) / 8ns (max.)
Row Cycle Time (tRCmin)	60ns (min.)
Refresh to Active/Refresh Command Time (tRFCmin)	127.5ns (min.)
Row Active Time (tRASmin)	45ns (min.) / 70,000 (max.)
Single Power Supply of	+1.8V (+/1V)
Power Operating Main Chips	2.016 W (Approx.*) Samsung,Hynix,Micron
UL Rating	94 V - 0

^{*}Power will vary depending on the SDRAM used.

DESCRIPTION

This document describes ValueRAM's 256M x 64-bit (2GB) DDR2-800 CL6 SDRAM (Synchronous DRAM) 2Rx8 memory module, based on sixteen 128M x 8-bit FBGA components. This 240-pin DIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

Note: The module described in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here.

